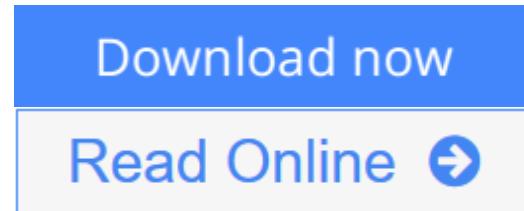


SAT-Based Scalable Formal Verification Solutions (Integrated Circuits and Systems)

By Malay Ganai, Aarti Gupta



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This book provides an engineering insight into how to provide a scalable and robust verification solution with ever increasing design complexity and sizes. It describes SAT-based model checking approaches and gives engineering details on what makes model checking practical. The book brings together the various SAT-based scalable emerging technologies and techniques covered can be synergistically combined into a scalable solution.



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Editorial Review

From the Back Cover

Functional verification has become an important aspect of the chip design process. Significant resources, both in industry and academia, are devoted to the design complexity and verification endeavors.

SAT-Based Scalable Formal Verification Solutions discusses in detail several of the latest and interesting scalable SAT-based techniques including: Hybrid SAT Solver, Customized Bounded/Unbounded Model Checking, Distributed Model Checking, Proofs and Proof-based Abstraction Methods, Verification of Embedded Memory System & Multi-clock Systems, and Synthesis for Verification Paradigm. These techniques have been designed and implemented in a verification platform Verisol (formally called DiVer) and have been used successfully in industry. This book provides algorithmic details and engineering insights into devising scalable approaches for an effective realization. It also includes the authors' practical experiences and recommendations in verifying the large industry designs using VeriSol.

The book is primarily written for researchers, scientists, and verification engineers who would like to gain an in-depth understanding of scalable SAT-based verification techniques. The book will also be of interest for CAD tool developers who would like to incorporate various SAT-based advanced techniques in their products.

Users Review

From reader reviews:

Daniele Chambers:

The feeling that you get from SAT-Based Scalable Formal Verification Solutions (Integrated Circuits and Systems) could be the more deep you excavating the information that hide into the words the more you get considering reading it. It does not mean that this book is hard to be aware of but SAT-Based Scalable Formal Verification Solutions (Integrated Circuits and Systems) giving you buzz feeling of reading. The writer conveys their point in selected way that can be understood by anyone who read it because the author of this guide is well-known enough. This specific book also makes your own personal vocabulary increase well. That makes it easy to understand then can go with you, both in printed or e-book style are available. We advise you for having this kind of SAT-Based Scalable Formal Verification Solutions (Integrated Circuits and Systems) instantly.

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