



CMOS RFIC Design Principles (Artech House Microwave Library)

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Recently, there has been a major push to integrate circuitry and digital signal processors on a single chip to improve wireless digital transmission. CMOS (complementary metal oxide semiconductor) is a key digital integrated circuit technology that is widely used throughout the wireless communications industry. This practical resource offers comprehensive, expert guidance on designing CMOS RF integrated circuits. Professionals get complete design details on elemental and advanced CMOS RF circuits, from low noise amplifiers, general gain amplifiers, mixers, and oscillators, to voltage controlled oscillators, phase lock loops, frequency synthesizers, and power amplifier architectures. Engineers also find a review of RF system fundamentals and definitions of terms widely used in the field. This accessible reference includes numerous examples that clearly illustrate and support important topics throughout the book. This work includes a CD-ROM, which contains various demonstration and student versions of CAD tools, CAD layouts, and measured RF data on select RFIC circuits.

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Editorial Review

About the Author

Robert H. Caverly is a faculty member at Villanova University in the Department of Electrical and Computer Engineering. Previously, he was affiliated with the University of Massachusetts Dartmouth (formerly Southeastern Massachusetts University). He received his M.S. and Ph.D. in electrical engineering from North Carolina State University and Johns Hopkins University, respectively.

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